



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Ligang Zhang, et al.

Title: ELECTROMAGNETIC SHIELDING STRUCTURE

Application No.: 10/813,886 Filed: March 31, 2004

Examiner: Jennifer E. Matisiak Group Art Unit: 2811

Atty. Docket No.: 026-0047 Confirmation No.: 5768

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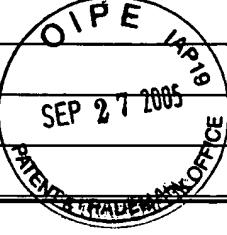
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Nicole Teitler Cave Date
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Respectfully submitted,


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INFORMATION DISCLOSURE STATEMENT BY APPLICANT		First Named Inventor: Ligang Zhang		
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		Group Art Unit: 2811		
		Examiner Name: Jennifer E. Matisiak		
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NON PATENT LITERATURE DOCUMENTS				
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	AA	FROMMBERGER, Michael, et al., "Integration of Crossed Anisotropy Magnetic Core Into Toroidal Thin-Film Inductors," IEEE Transactions on Microwave Theory and Techniques, Vol. 53, No. 6, June 2005, pp. 2096-2100.		
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	AD	SOH, H. T., et al., "Through-Wafer Vias (TWV) and their Applications in 3 Dimensional Structures," Proceedings of 1998 International Conference on Solid State Devices and Materials, September 1998, 12 pages.		
	AE	TANG, Chih-Chun, et al., "Miniature 3-D Inductors in Standard CMOS Process," IEEE Journal of Solid-State Circuits, Vol. 37, No. 4, April 2002, pp. 471-480.		
	AF	TRELEAVEN, Dave and Dick James, "Integrated Circuit Passive Components," Whitepapers, Chipworks, www.chipworks.com/resources/resources_whitepapers.asp , 4 pages.		
	AG	TSUI, Hau-Yiu and Jack Lau, 2003 IEEE MTT-S Digest, pp. 225-228.		
	AH	WU, Joyce H., et al., "A Through-Wafer Interconnect in Silicon for RFICs," IEEE Transactions on Electron Devices, Vol. 51, No. 11, November 2004, pp. 1765-1771.		
	AI	ZANNOTH, Markus, et al., "A Fully Integrated VCO at 2 GHz," IEEE Journal of Solid-State Circuits, Vol. 33, No. 12, December 1998, pp. 1987-1991.		
	AJ	ZOU, Jun, et al., "Development of Vertical Planar Coil Inductors Using Plastic Deformation Magnetic Assembly (PDMA), 2001 IEEE International Microwave Symposium, May 2001, 4 pages.		
Examiner Signature		Date Considered		

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